

PATENT

Attorney Docket No. MTI-31532

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Weimer, Ronald A.  
Serial No.: 09/941,827  
Filing Date: August 29, 2001  
For: Method of Improved High K Dielectric-Polysilicon Interface for CMOS Devices  
Examiner: PHAM, Thanhha S.  
Group Art Unit: 2813  
Confirmation No.: 7551

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**CERTIFICATION OF SUBMISSION**

I hereby certify that, on the date shown below, this correspondence is being transmitted via the Patent Electronic Filing System (EFS) addressed to Examiner PHAM at the U.S. Patent and Trademark Office.

Date: May 10, 2006 Jim L. Palmatier

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE**

Sir:

Applicant requests consideration of the pending claims in the above-identified patent application based on the remarks herein.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 29 of this paper.